FIG. 1 (PRIOR ART)

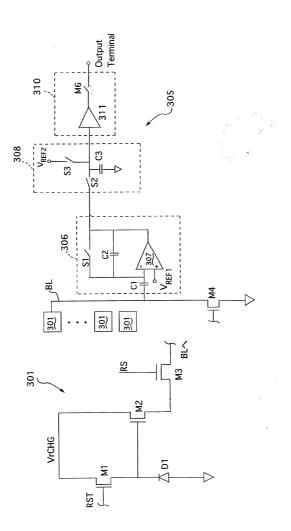
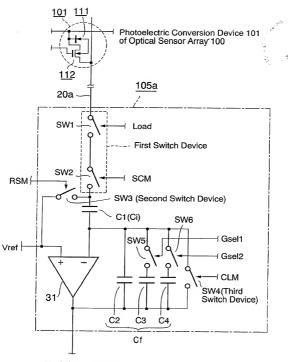


FIG. 2



To Column Type ADC

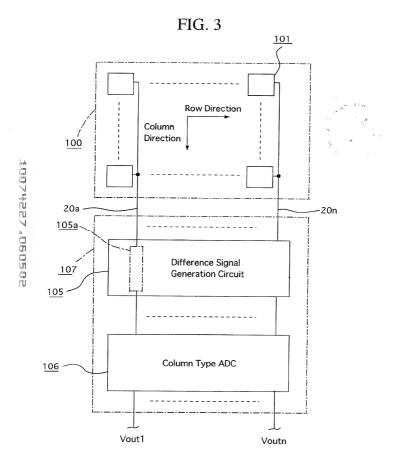


FIG. 4A

FIG. 4B

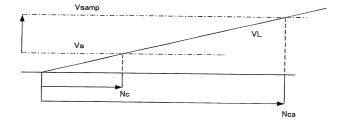


FIG. **5**

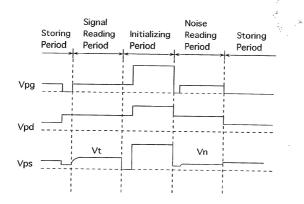


FIG. 6

	Storing Period	Signal Reading Period	Initializing Period	Noise Reading Period	Storing Period
Initializati Input Cap	1 1 1	Read of Sensor Signal	Reset of Pixel	Initialization o	of
Load					
(sw1)					
SCM	1 1			1	
(sw2)			, , , , , , , , , , , , , , , , , , ,	 	
RSM _		 			
(sw3)	1 1	1			
CLM _		<u> </u>			
(sw4)					
		1			

FIG. 7

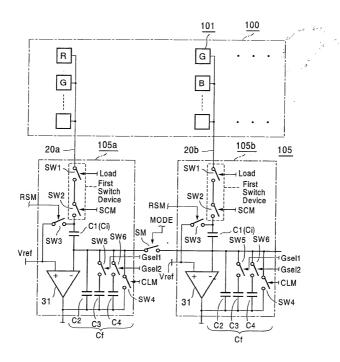


FIG. 8

	Storing Period	Signal Reading Period	Initializing Period	Noise Reading Period	Storing Period
Initialization	of ¬	Read of	Reset	Initialization of	
Input Capaci	itor	Sensor Signal	of Pixel	Signal Line	
Load (sw1)					
SCM					
(sw2)				<u>i - </u>	
RSM _					
(sw3)	1 1	1			
CLM (sw4)					
MODE_ (SM)					

FIG. 9

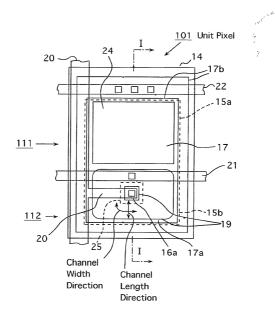


FIG. 10

